

FIG. 1

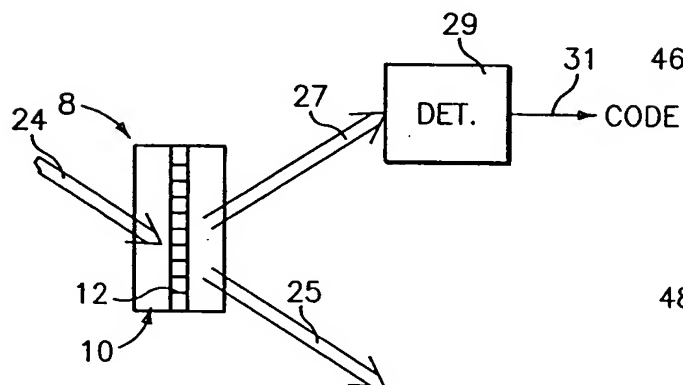


FIG. 2

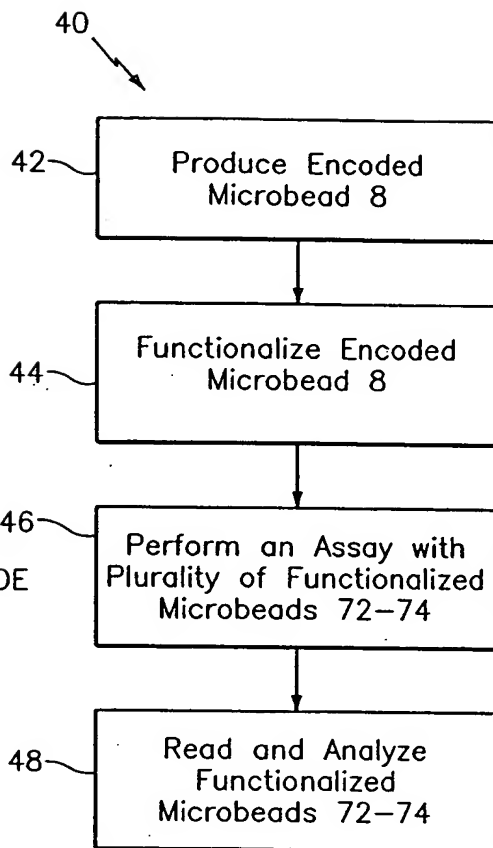


FIG. 3

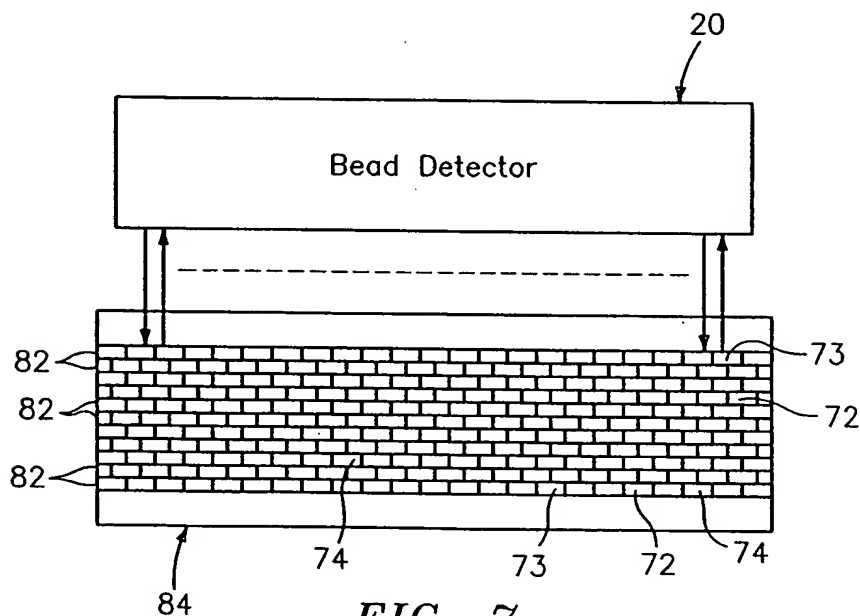


FIG. 7

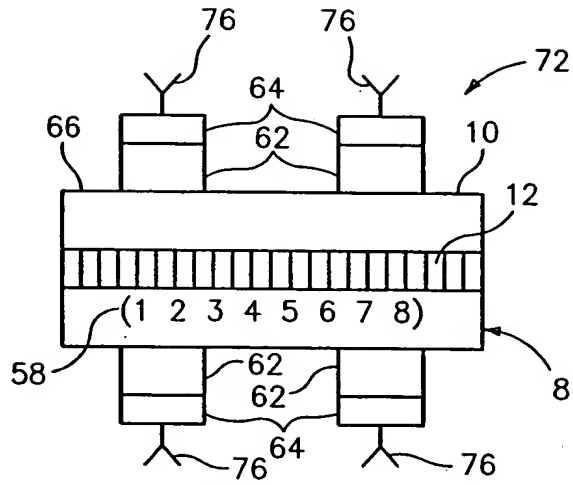


FIG. 4

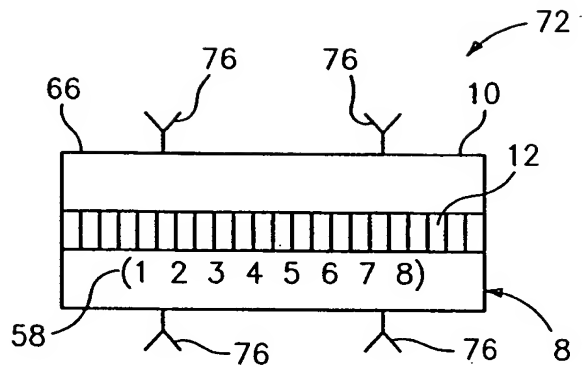


FIG. 5

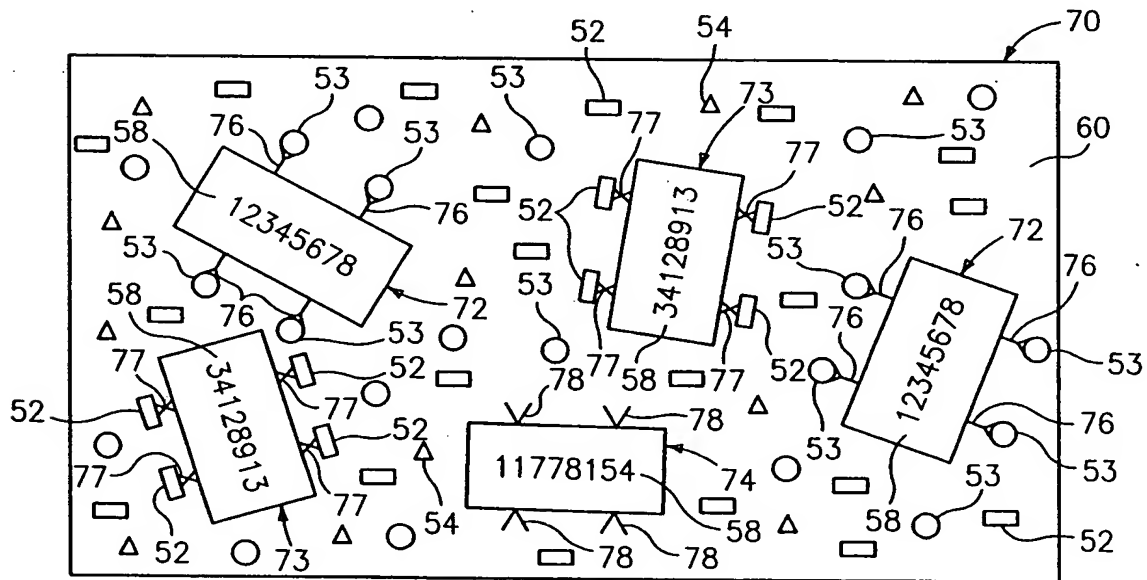


FIG. 6

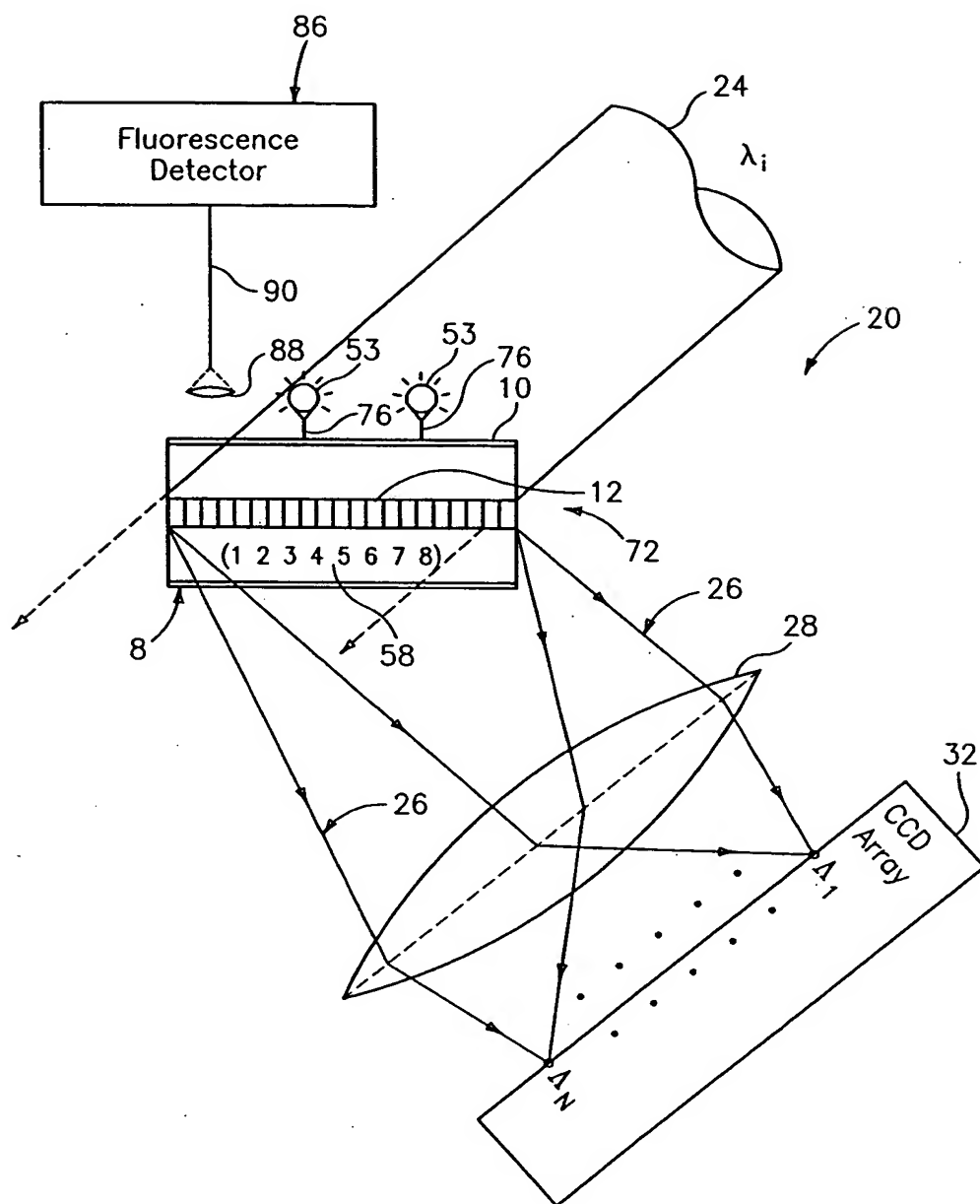


FIG. 8

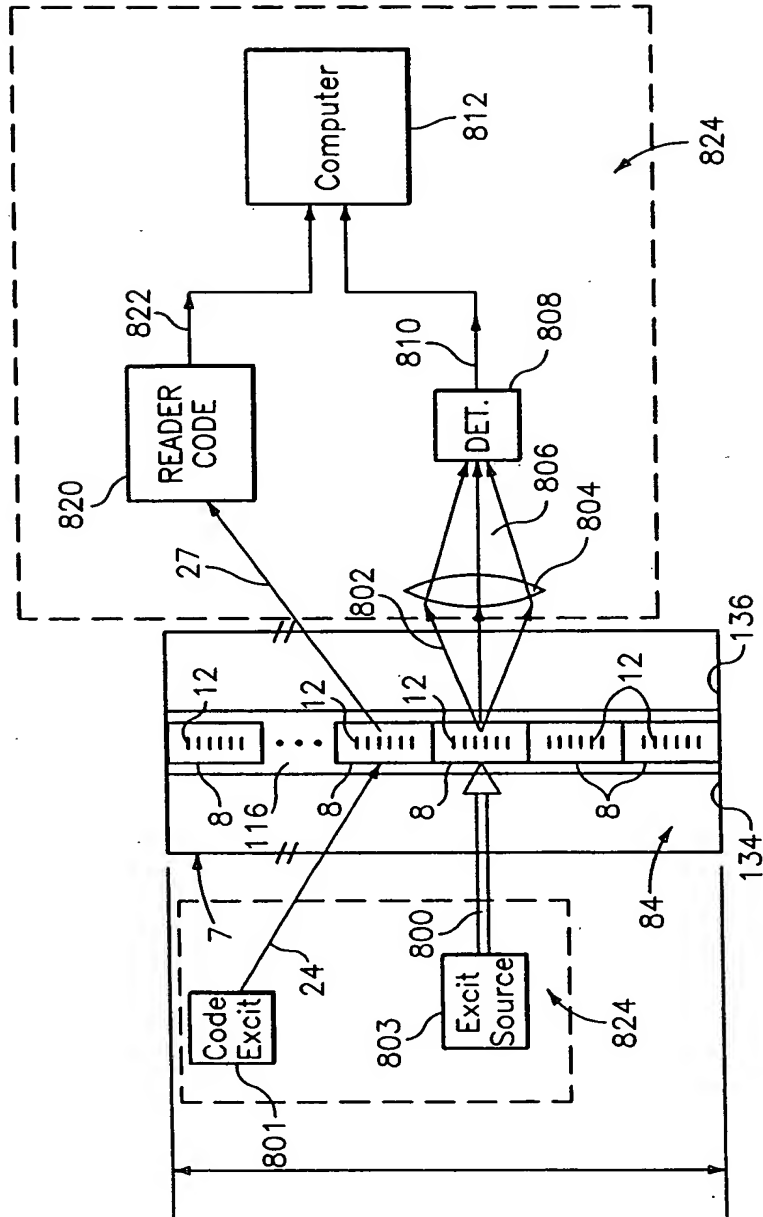


FIG. 9

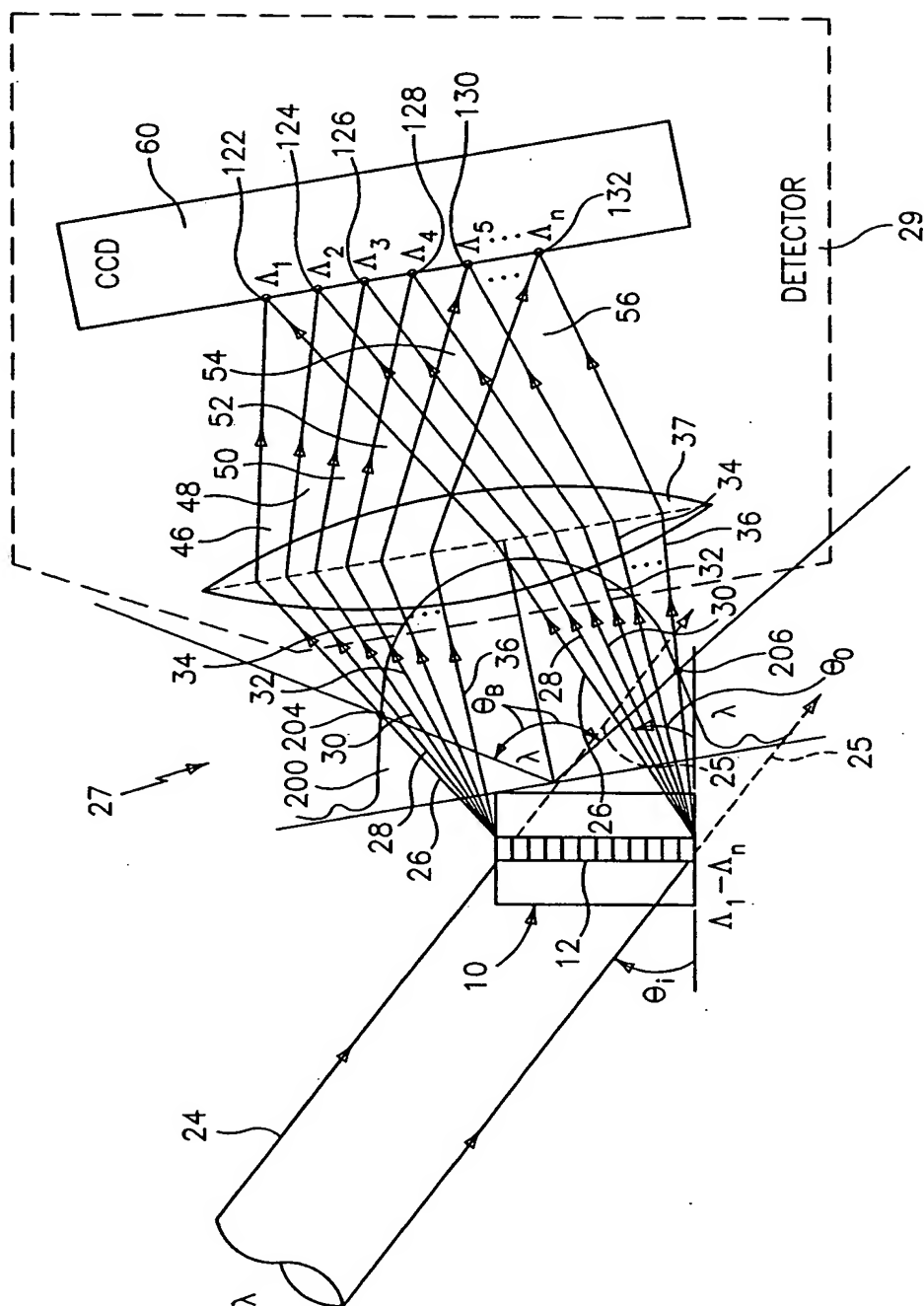


FIG. 10

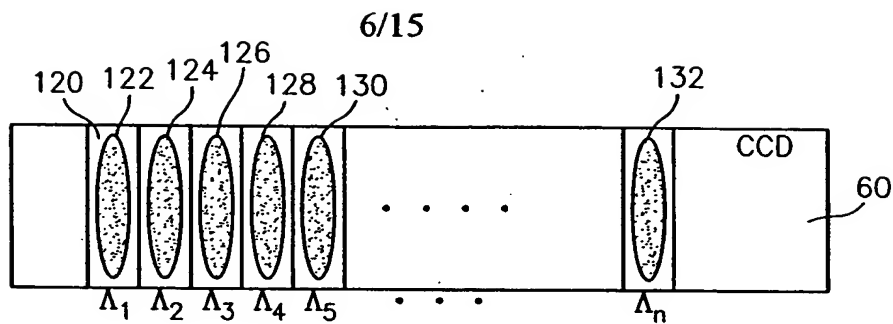


FIG. 11

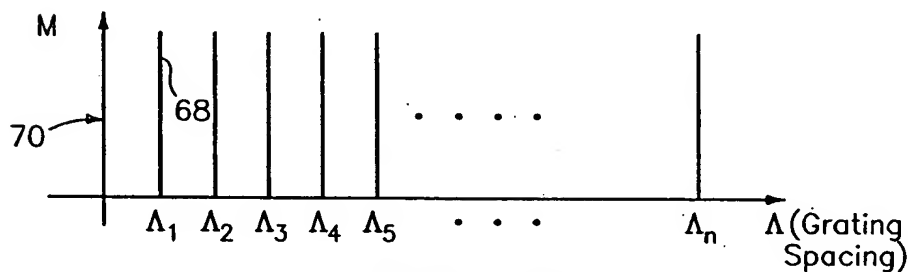
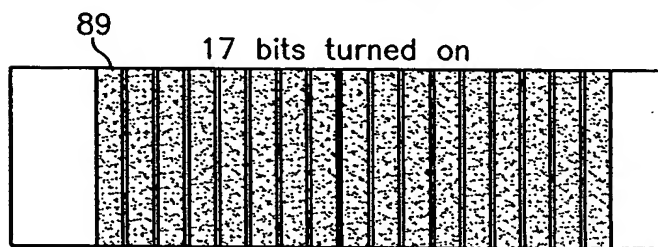
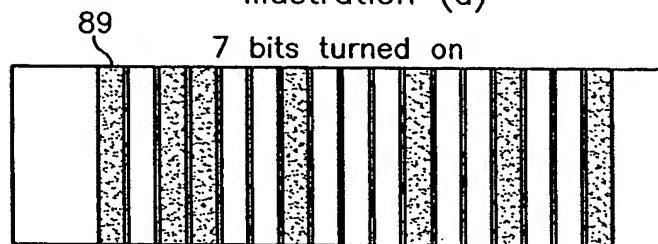


FIG. 12



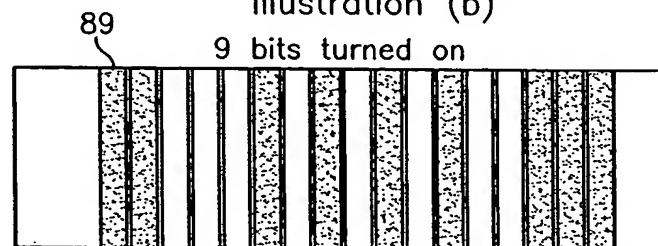
Bits: 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

Illustration (a)



Bits: 1 0 1 1 0 0 1 0 0 0 1 0 0 1 0 0 1

Illustration (b)



Bits: 1 1 0 0 0 1 0 1 0 1 0 1 0 0 1 1 1

Illustration (c)

FIG. 13

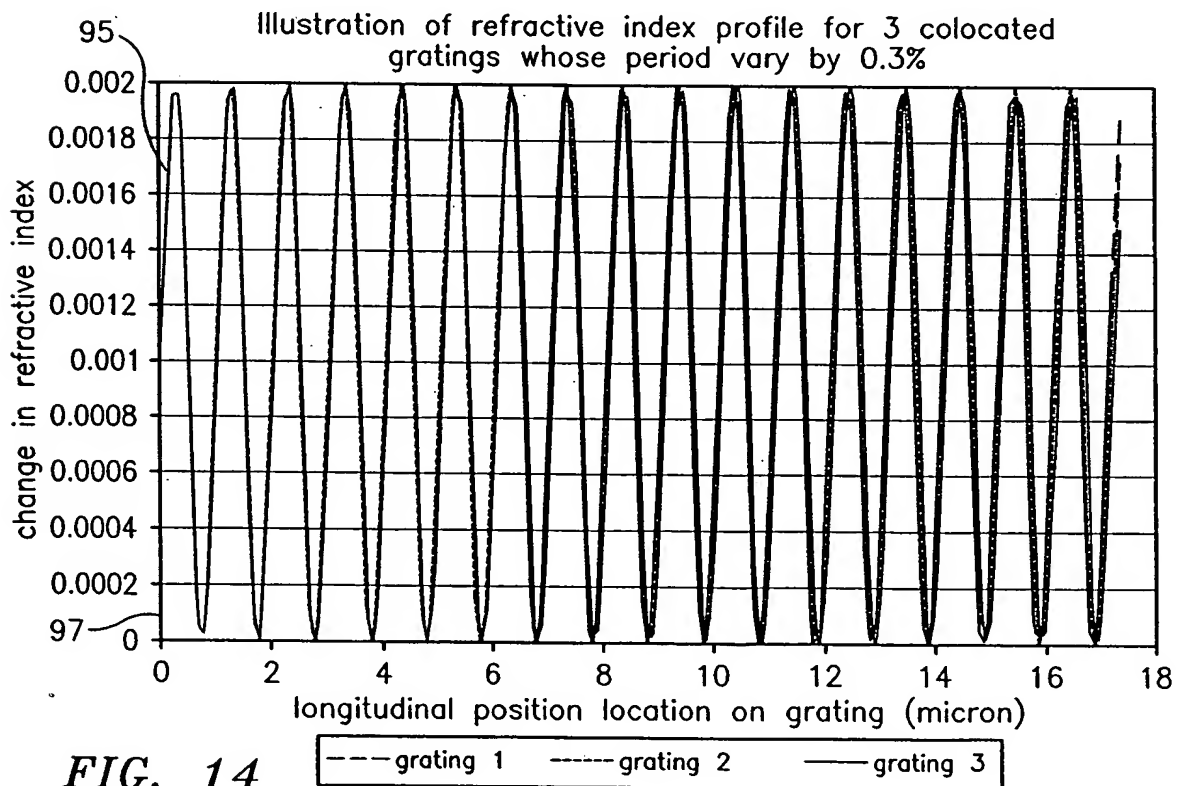
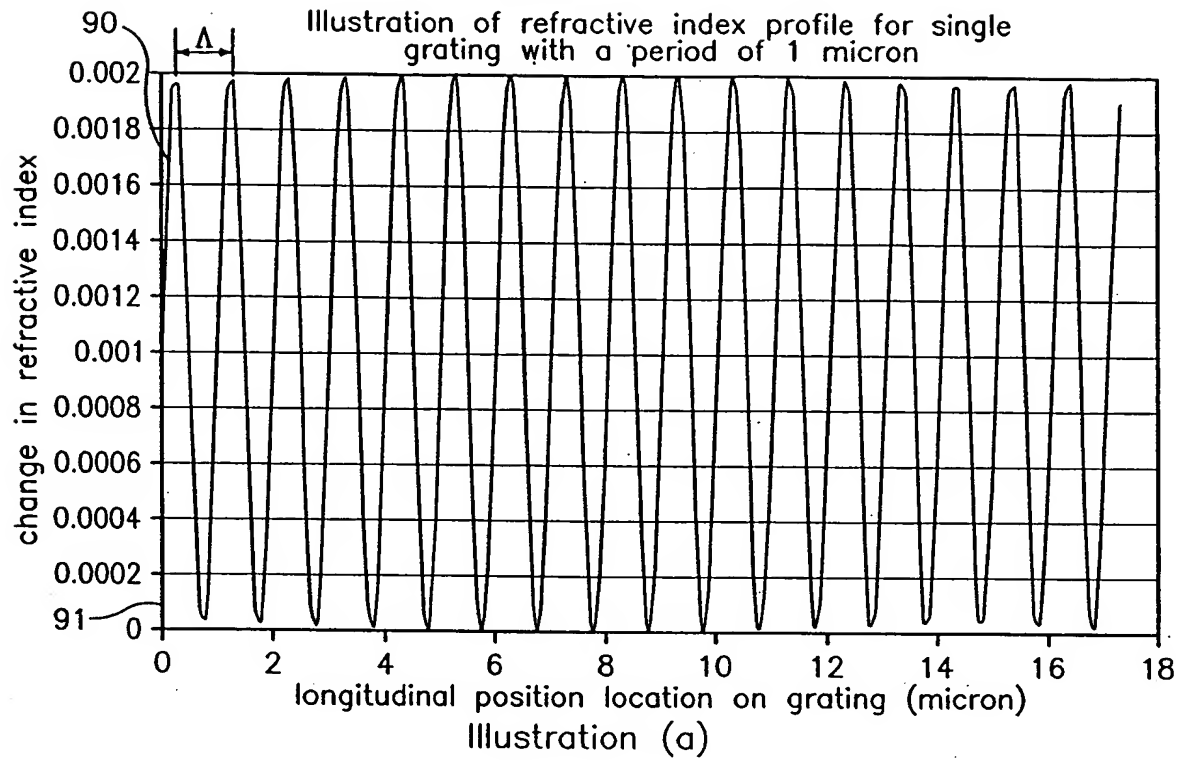


Illustration (c)

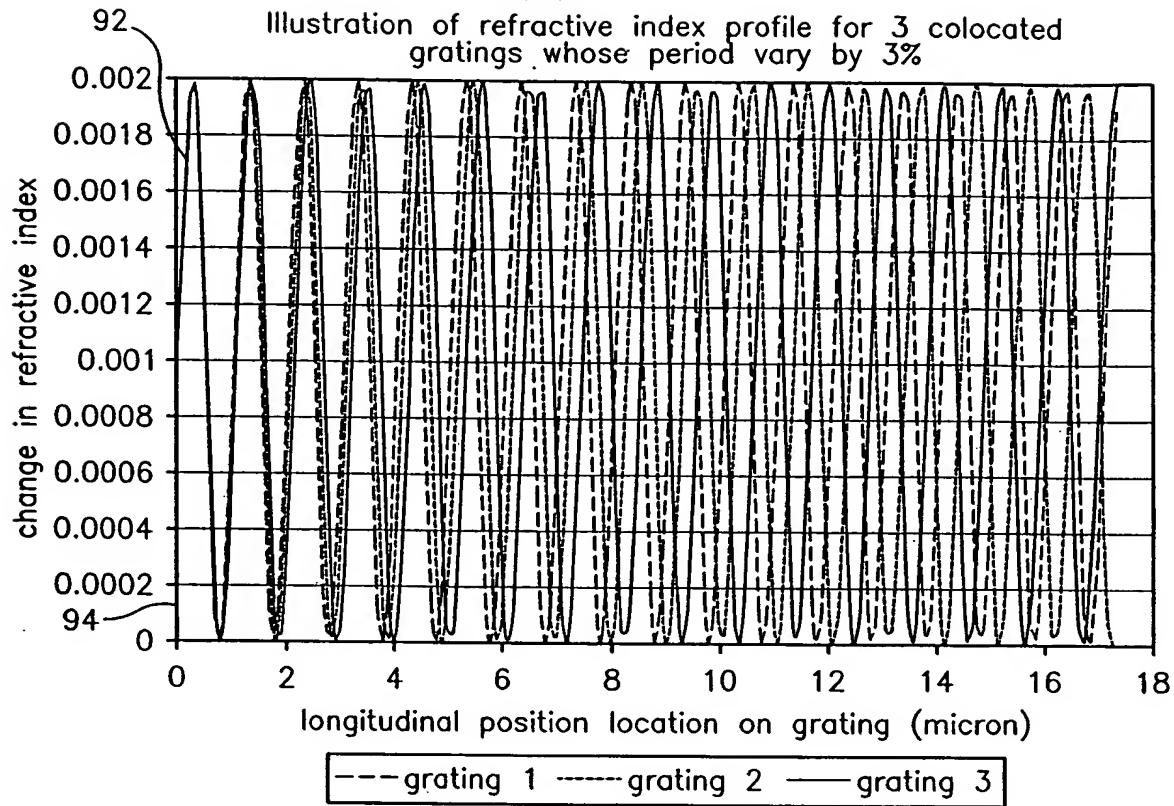


Illustration (b)

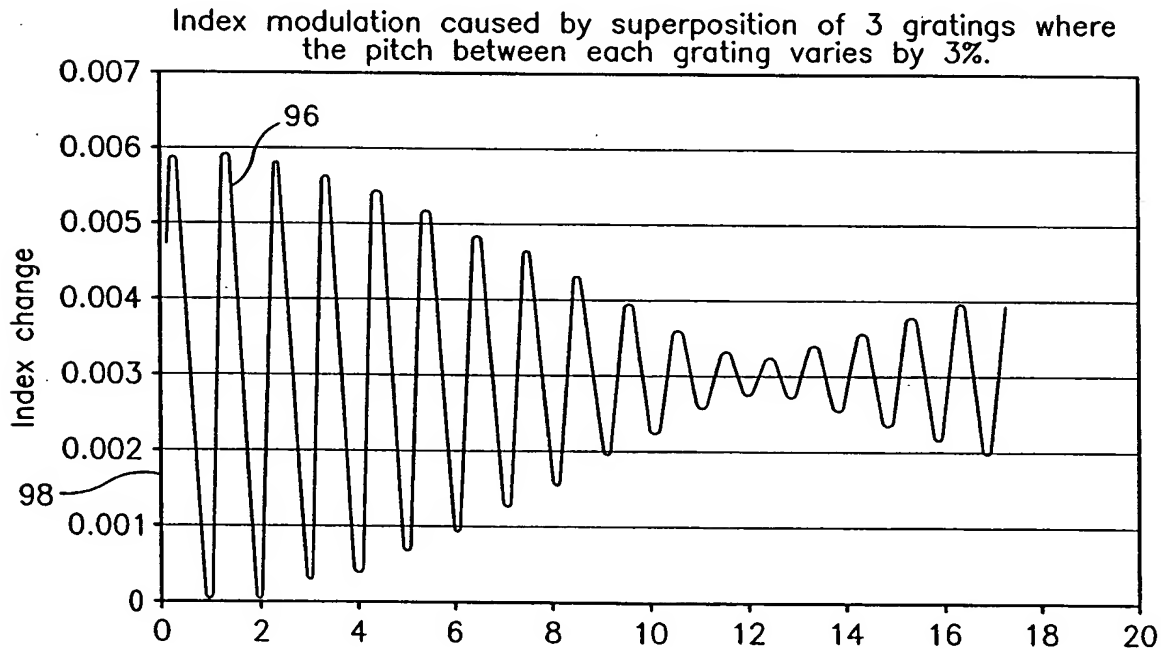


Illustration (d)



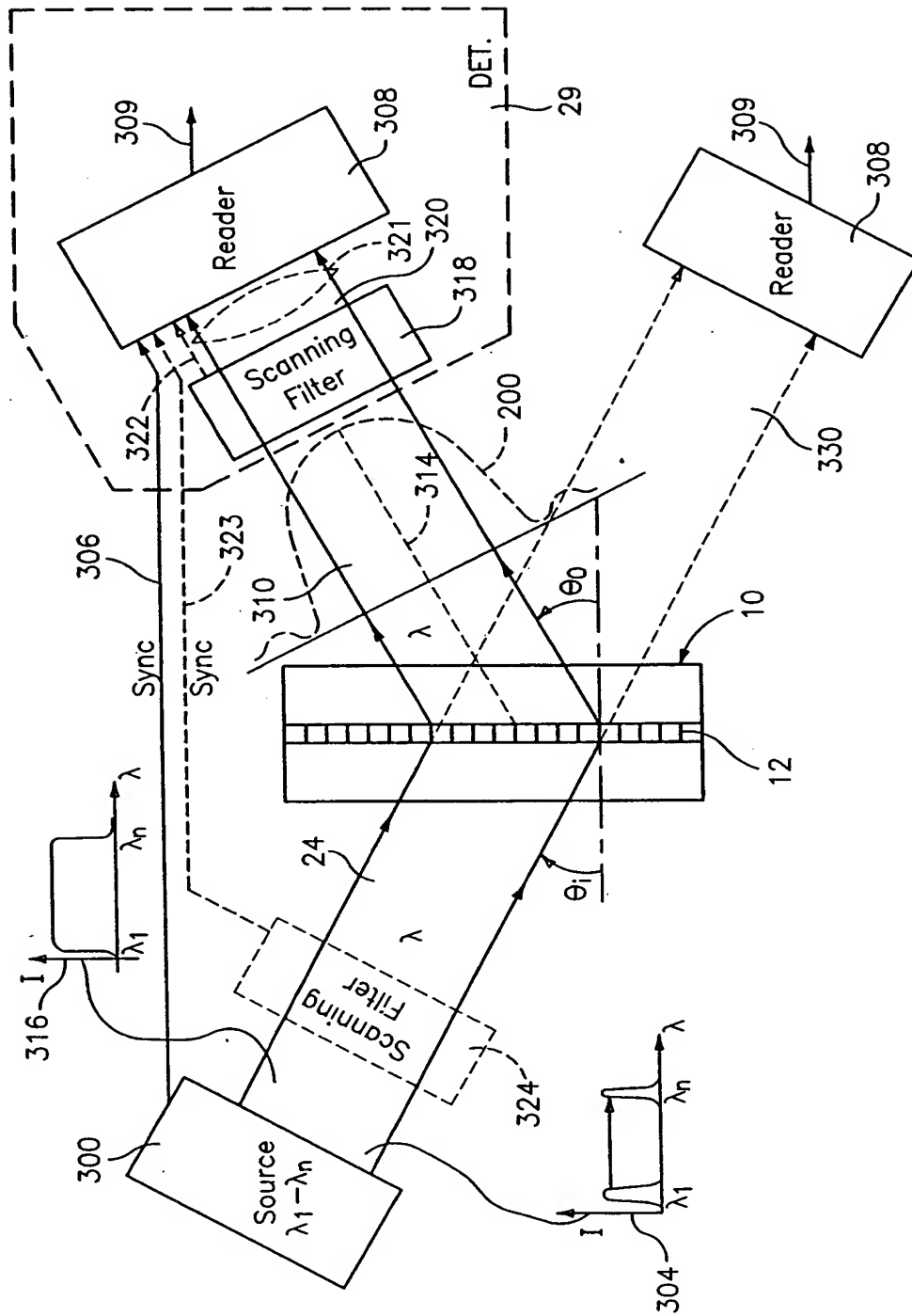
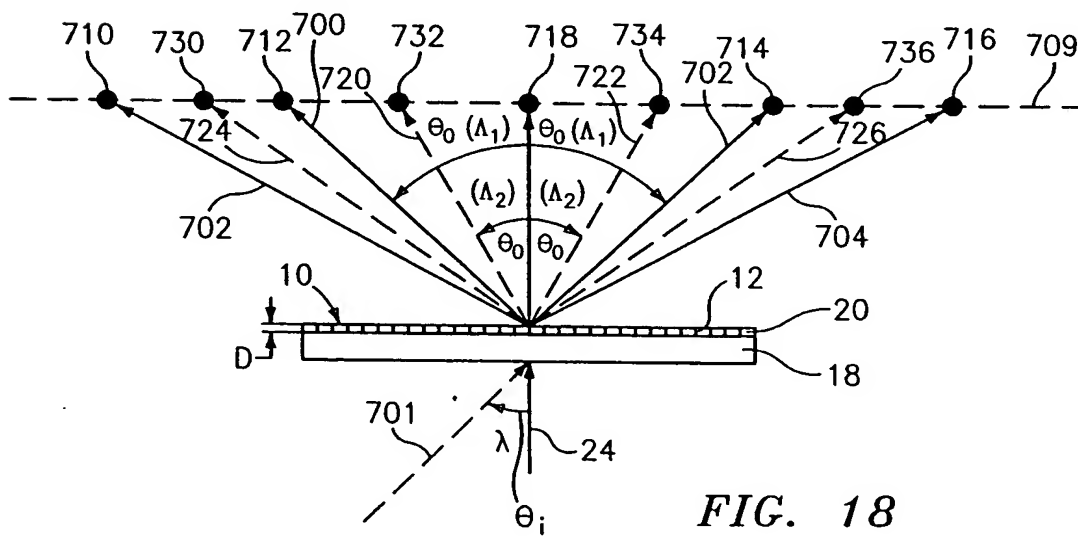
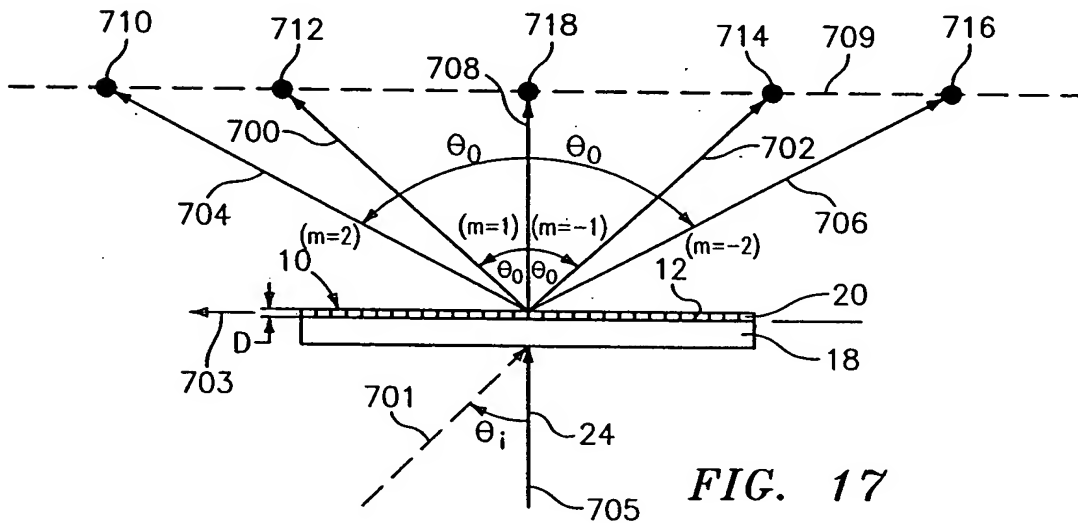
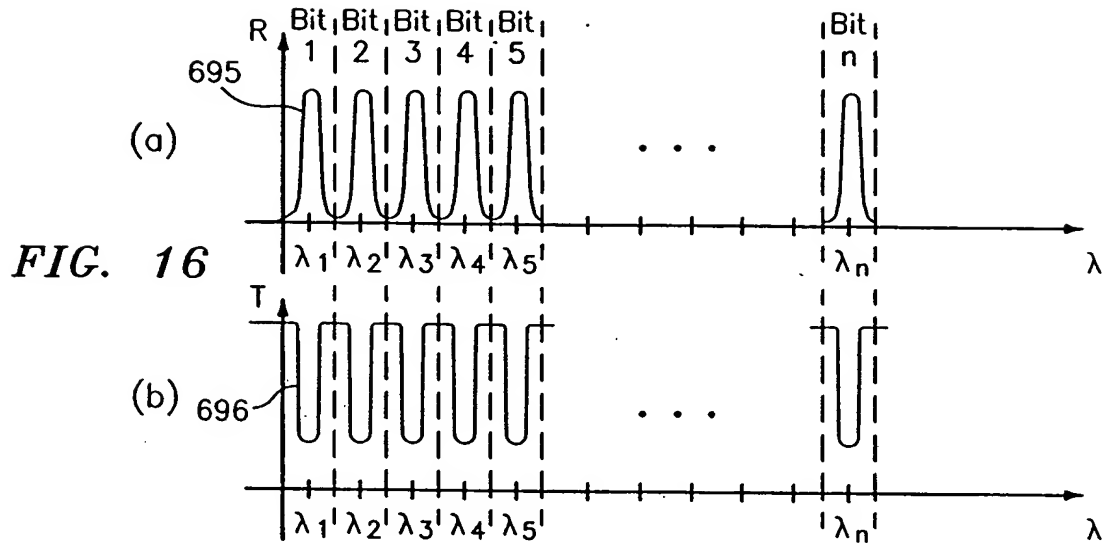
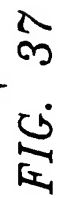
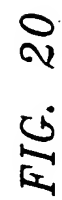
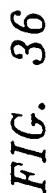
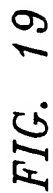


FIG. 15





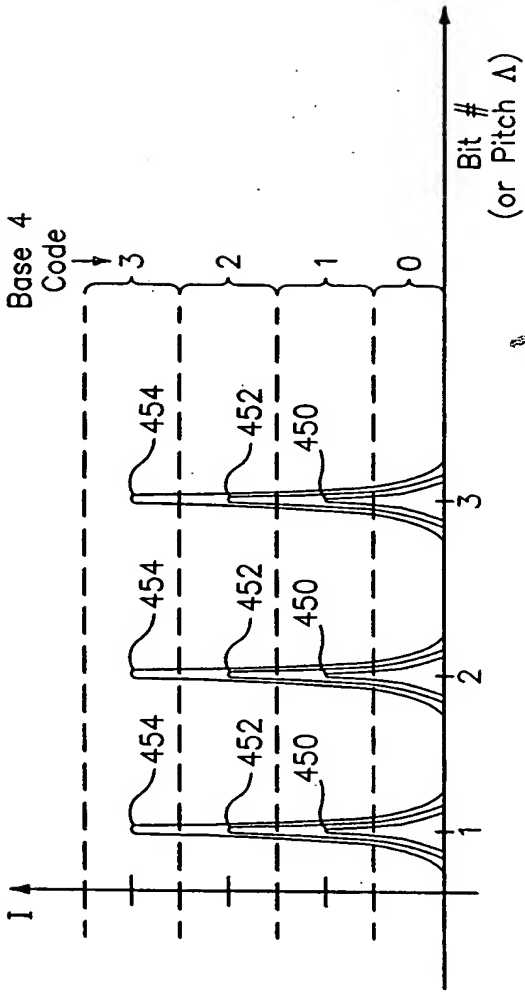


FIG. 21

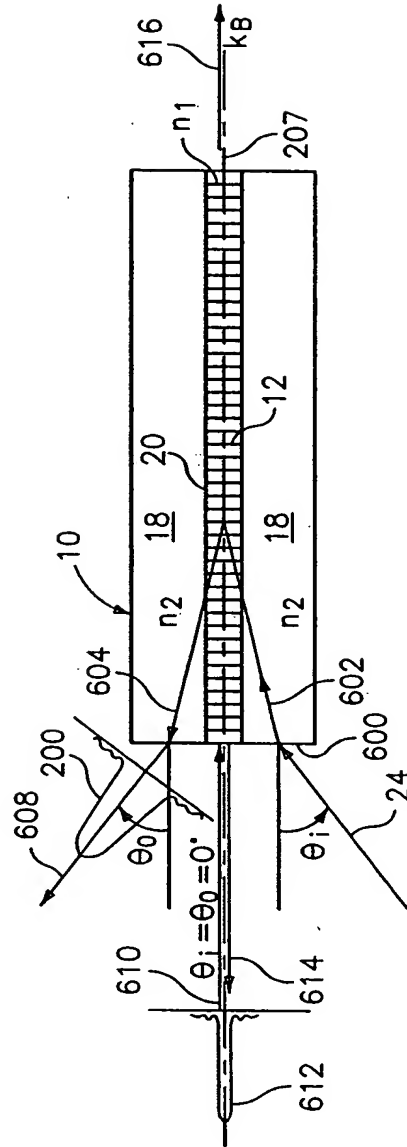


FIG. 22

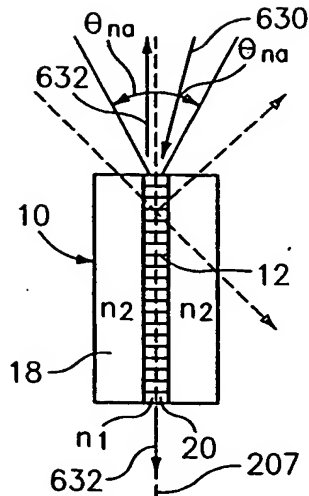


FIG. 23

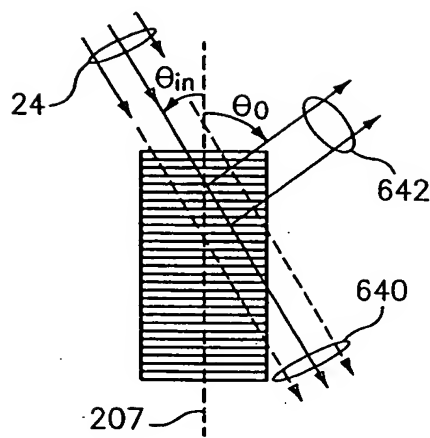
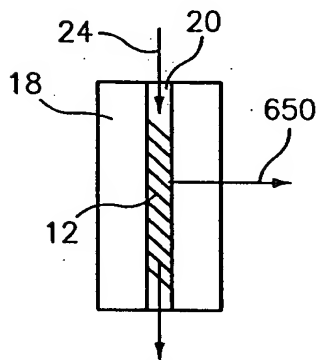
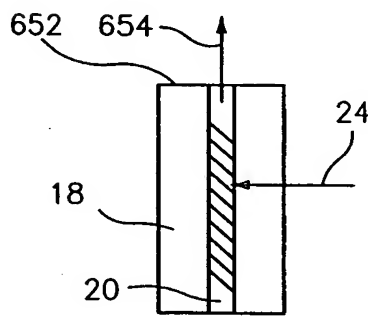


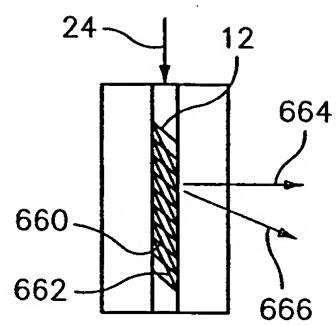
FIG. 24



Illustration(a)



Illustration(b)



Illustration(c)

FIG. 25

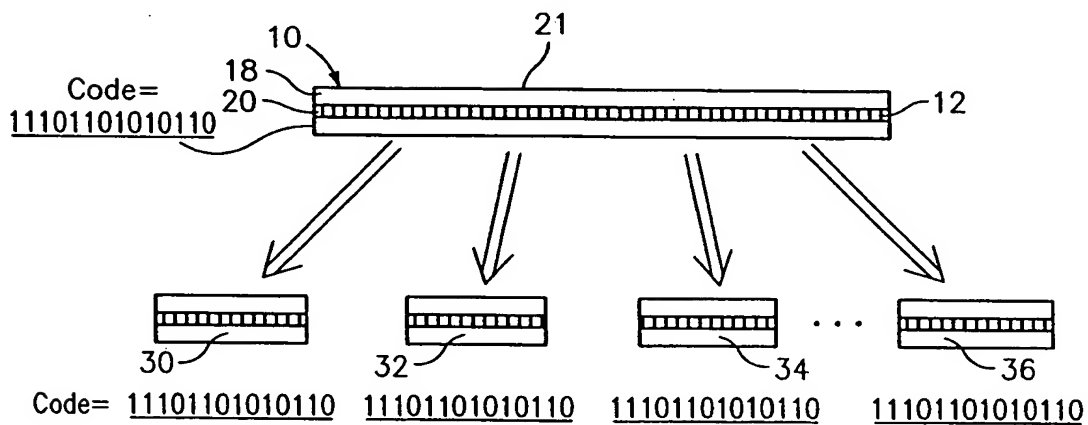
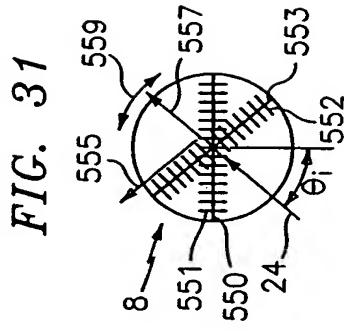
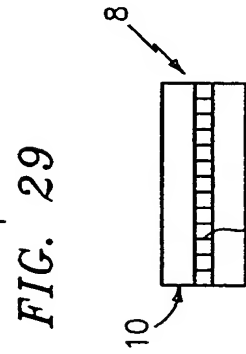
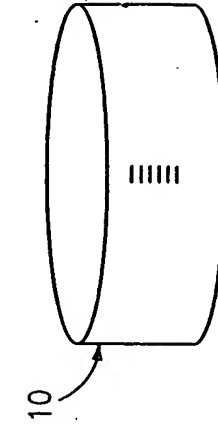
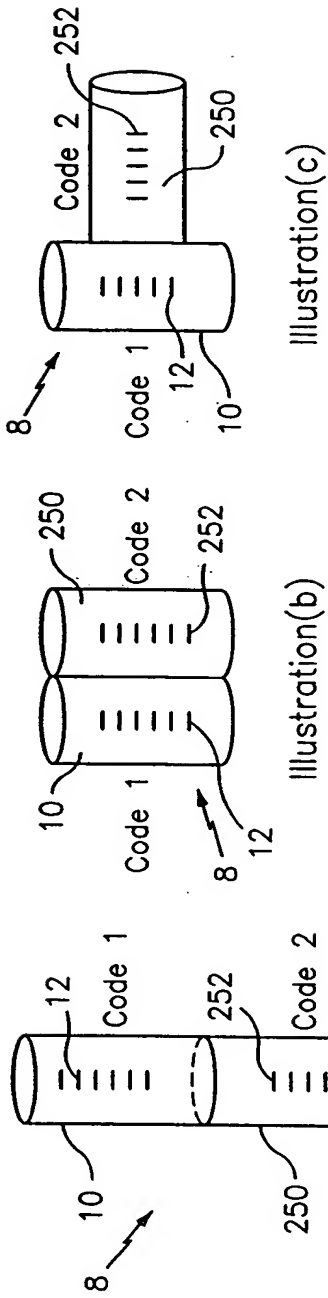
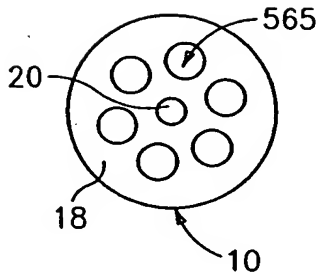
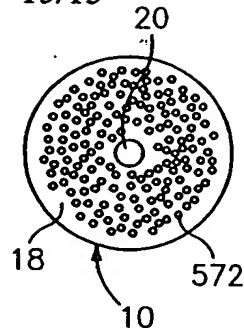


FIG. 27

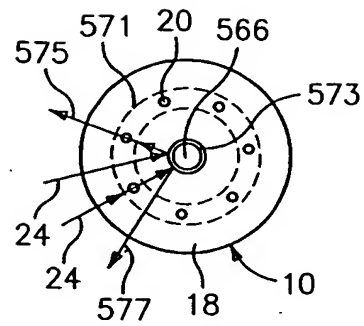




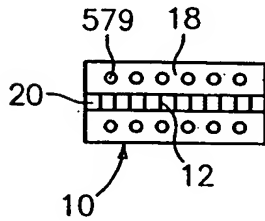
Illustration(a)



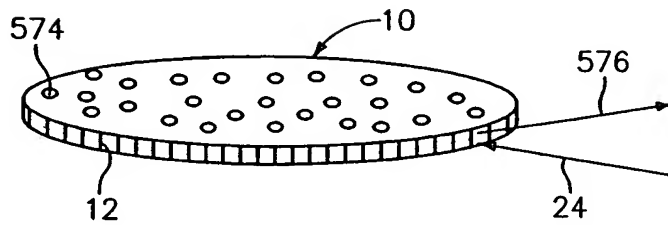
Illustration(b)



Illustration(c)

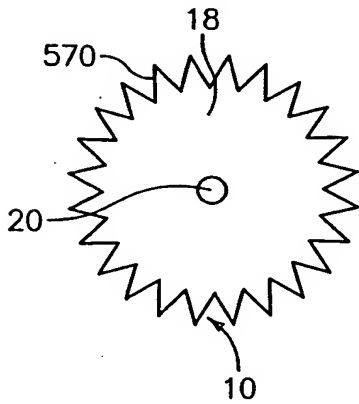


Illustration(d)

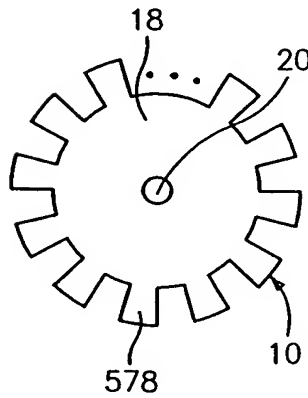


Illustration(e)

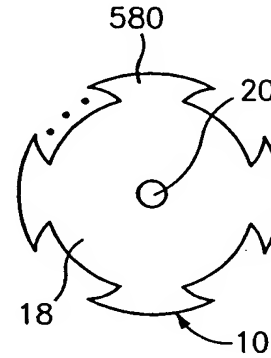
**FIG. 33**



Illustration(a)

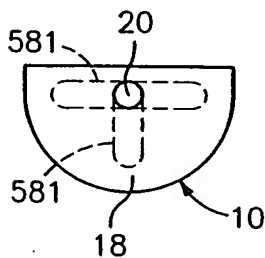


Illustration(b)

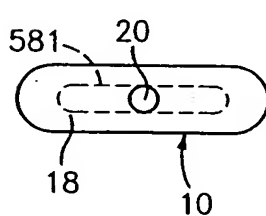


Illustration(c)

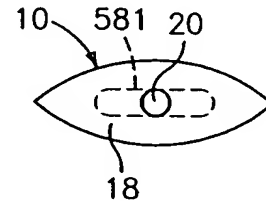
**FIG. 34**



Illustration(a)



Illustration(b)



Illustration(c)

**FIG. 35**